IN THE CLAIMS

The listing of claims will replace all prior versions, and listing, of claims in the application:

1 (original): A motherboard, comprising:

a printed circuit board;

a first memory slot set disposed on the printed circuit board having a first memory slot and a second memory slot;

a second memory slot set disposed on the printed circuit board having a third memory slot and a fourth memory slot;

and a terminator circuit module disposed between the first memory slot set and the second memory slot set, wherein the terminator circuit module is electrically connected to the first memory slot set and the second memory slot set through the printed circuit board.

2 (original): The motherboard as claimed in claim 1, wherein the terminator circuit module comprises a plurality of resistors and a plurality of capacitors, and the plurality of resistors and the plurality of capacitors are connected in series.

3 (original): The motherboard as claimed in claim 2, wherein the plurality of resistors and the plurality of capacitors are electrically connected in series and alternately arranged.

4 (currently amended): A motherboard, comprising:

a circuit board;

a chipset disposed on the circuit board;

a first memory slot set disposed on the circuit board having a first memory slot and a second memory slot;

a second memory slot set disposed on the circuit board having a third memory slot and a fourth memory slot;

a plurality of terminal resistors disposed between the first memory slot set and the second memory slot set; and

a serial resistance disposed between the chipset and the first and the second memory slot sets, wherein the plurality of terminal resistors is electrically connected to the first memory slot set and the second memory slot set through the circuit board, and the plurality of terminal resistors, the first memory slot set and the second memory slot set are connected to a terminator voltage.

5 (currently amended): The motherboard as claimed in claim 4, further comprising a plurality of capacitors, wherein the plurality of terminal resistors and the plurality of capacitors are alternately arranged.

6 (currently amended): A slot apparatus for a memory module on a printed circuit board, comprising:

a first memory slot set disposed on the printed circuit board having a first memory slot and a second memory slot;

a second memory slot set disposed on the printed circuit board having a third memory slot and a fourth memory slot;

a plurality of terminal resistors disposed between the first memory slot set and the second memory slot set; and

a serial resistance disposed on the printed circuit board and electrically connected to the first memory slot set and the second memory slot set through the printed circuit board, wherein the plurality of terminal resistors are [[is]] respectively and electrically connected to the first

memory slot set and the second memory slot set through the printed circuit board, and the terminal resistors, the first memory slot set and the second memory slot set are connected to a terminator voltage.

7 (currently amended): The slot apparatus for a memory module as claimed in claim 6, further comprising a plurality of capacitors, wherein the plurality of terminal resistors and the plurality of capacitors are alternately arranged.

8 (original): The slot apparatus for a memory module as claimed in claim 6, wherein the printed circuit board is a motherboard.

9 (original): The slot apparatus for a memory module as claimed in claim 8, further comprising a chipset disposed on the printed circuit board, wherein the serial resistance disposed between the chipset and the first and the second memory slot sets.

10 (new): The motherboard as claimed in claim 1, wherein the first, second, third and fourth memory slot are used for plugging in a memory device.

11 (new): The motherboard as claimed in claim 2, wherein the terminator circuit module further comprises a terminator voltage connected to the first memory slot set, the second memory slot set and the plurality of resistors.